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Document Number 1

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File: JPAB

Nov 20, 1992

PUB-NO: JP404333118A

DOCUMENT-IDENTIFIER: JP 04333118 A

TITLE: INFORMATION PROCESSOR

PUBN-DATE: November 20, 1992

INVENTOR-INFORMATION:

NAME

SAKAGUCHI, TADAHIKO

ASSIGNEE-INFORMATION:

NAME COUNTRY

NEC CORP N/A

APPL-NO: JP03132277

APPL-DATE: May 9, 1991

INT-CL (IPC): G06F 1/18; G06F 1/26

ABSTRACT:

PURPOSE: To attain the replacement of a faulty package during the operation of a system and to improve the operating efficiency of the system by using the independent power supplies for the slots and supplying or interrupting the power to only a designated slot with the internal/external control.

CONSTITUTION: This processor is provided with a cage 1 to store the slots A-H, a power unit 2 which supplies the power to the slots A-H, a switch box 3 which controls the supply and the interruption of power to each slot, a back panel 6, and the slot power supply terminals 5A-5H. If the slot H, for example, has a fault, an operator operates a switch H housed in the box 3. Then a control signal 13H is released and the supply of power is interrupted to the slot H. The operator operates the switch H again after the replacement of control packages. Then the signal 13H is connected and the power is supplied to the slot H. In other words, a faulty package can be replaced with a nondefect one without stopping the operation of a system.

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